

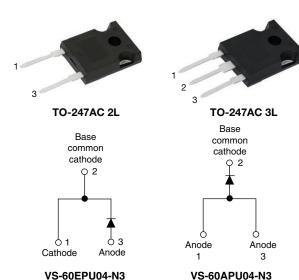
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Vishay Semiconductors

COMPLIANT

HALOGEN FREE

Ultrafast Soft Recovery Diode, 60 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	60 A			
V_R	400 V			
V _F at I _F	0.87 V			
t _{rr} typ.	See Recovery table			
T _J max.	175 °C			
Package	TO-247AC 2L, TO-247AC 3L			
Circuit configuration	Single			

FEATURES

- · Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Designed and qualified according to JEDEC®-JESD 47



BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- · Reduced parts count

DESCRIPTION / APPLICATIONS

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems.

The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

MECHANICAL DATA

Case: TO-247AC 2L, TO-247AC 3L

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	V _R		400	V	
Continuous forward current	I _{F(AV)}	T _C = 127 °C	60		
Single pulse forward current	I _{FSM}	$T_C = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$	600	Α	
Maximum repetitive forward current	I _{FRM}	Square wave, 20 kHz	120		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	400	-	-		
		I _F = 60 A	-	1.05	1.25	V	
Forward voltage	V_{F}	I _F = 60 A, T _J = 175 °C	-	0.87	1.03		
		I _F = 60 A, T _J = 125 °C	-	0.93	1.10		
Poveree leekage ourrent	1-	$V_R = V_R$ rated	-	-	50	μA	
Reverse leakage current I _R		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	2	mA	
Junction capacitance	C _T	V _R = 400 V	-	50	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	3.5	ı	nΗ	

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DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A, di}_F/\text{dt} = 200 \text{ A/}\mu\text{s, V}_R = 30 \text{ V}$		-	50	60	
Reverse recovery time t _{rr}	t _{rr}	T _J = 25 °C		-	85	-	ns A
		T _J = 125 °C	$I_F = 60 \text{ A}$ $di_F/dt = 200 \text{ A/µs}$ $V_R = 200 \text{ V}$	=	145	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	8.8	-	
		T _J = 125 °C		-	15.4	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	375	-	nC
		T _J = 125 °C		=	1120	-	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Thermal resistance, junction to case	R _{thJC}		-	-	0.70	K/W	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	40	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.2	-	K/W	
Moight			-	5.5	=	g	
Weight			-	0.2	-	OZ.	
Manustina taurus			1.2	-	2.4	N⋅m	
Mounting torque			10	=-	20	lbf · in	
Marking daving		Case style TO-247AC 2L	60EPU04			•	
Marking device		Case style TO-247AC 3L		60APU04			

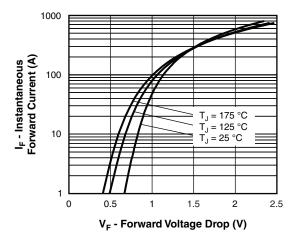


Fig. 1 - Typical Forward Voltage Drop Characteristics

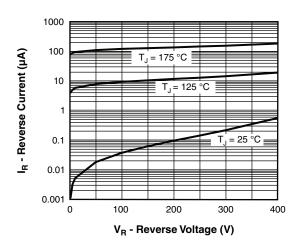


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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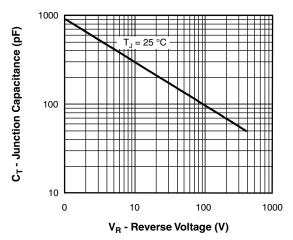


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

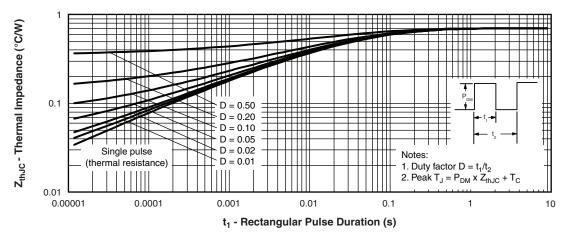


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

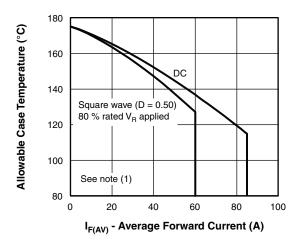


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

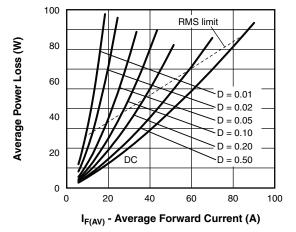


Fig. 6 - Forward Power Loss Characteristics

Note

Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

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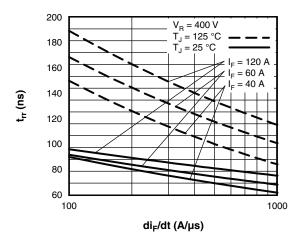


Fig. 7 - Typical Reverse Recovery Time vs. di_F/dt

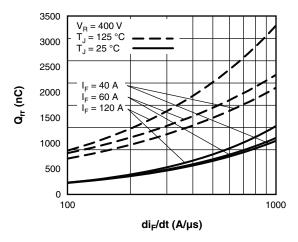
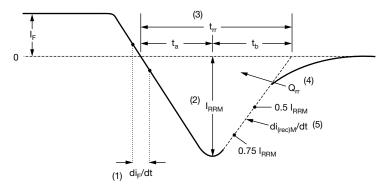


Fig. 8 - Typical Stored Charge vs. di_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_F$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RBM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

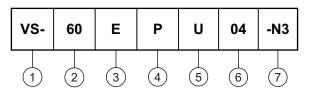
Fig. 9 - Reverse Recovery Waveform and Definitions



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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (60 = 60 A)

3 - Circuit configuration:

• E = single diode, 2 pins

• A = single diode, 3 pins

4 - Package:

P = TO-247AC

5 - Type of silicon:

U = ultrafast recovery

6 - Voltage rating (04 = 400 V)

7 - Environmental digit:

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-60EPU04-N3	25	500	Antistatic plastic tube		
VS-60APU04-N3	25	500	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS			
Dimensions	TO-247AC 2L	www.vishay.com/doc?96144	
Differsions	TO-247AC 3L	www.vishay.com/doc?96138	
Part marking information	TO-247AC 2L	www.vishay.com/doc?95648	
Part marking information	TO-247AC 3L	www.vishay.com/doc?95007	
SPICE model		www.vishay.com/doc?96899	



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